

Appl. No. 10/708,662
Amdt. dated January 27, 2006
Reply to Office action of October 31, 2005

AMENDMENTS TO THE SPECIFICATION

In the Title:

5 METHOD FOR A ~~DISPLAY CONTROLLER~~ GRAPHICS CHIP TO ACCESS
DATA STORED IN A SYSTEM MEMORY OF A COMPUTER DEVICE

In paragraph [0002]:

10 The present invention relates to a method for accessing the data stored in the
memory, and more specifically, to a method for accessing the data stored in the system
memory by a ~~display controller~~ graphics chip.

In paragraph [0008]:

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It is therefore a primary objective of the claimed invention to provide a method for
accessing the data stored in the system memory with a ~~display controller~~ graphics chip.

In paragraph [0009]:

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According to the claimed invention, a method for a ~~display controller~~ graphics chip
to access data stored in a system memory of a computer device comprises the following
steps: (a) setting a block capacity value; (b) dividing a plurality of read requests
corresponding to a predetermined request sequence and said block capacity value into a
25 plurality of request, wherein a total amount of data required by read requests grouped in
each request group is less than the block capacity value; (c) reordering the read requests in
each of said request groups corresponding to data on the page of said system memory into
a second request sequence for each of said request groups; and (d) executing the read

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requests in each of request group according to said second request sequence of each of said request groups.

In the Abstract of Disclosure (clean copy on next page):

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A method for a ~~display controller~~ graphics chip to access data stored in a system memory of a computer device is disclosed. The method includes using a memory controller to set a block capacity value; using the memory controller to divide a plurality of read requests corresponding to a predetermined request sequence into a plurality of request groups, wherein a total amount of data required by read requests grouped in each request group is less than the block capacity value; and using the memory controller to adjust a request sequence corresponding to read requests grouped in each request group for retrieving data stored at different N pages so that a memory device only performs N-1 page switching operations.

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